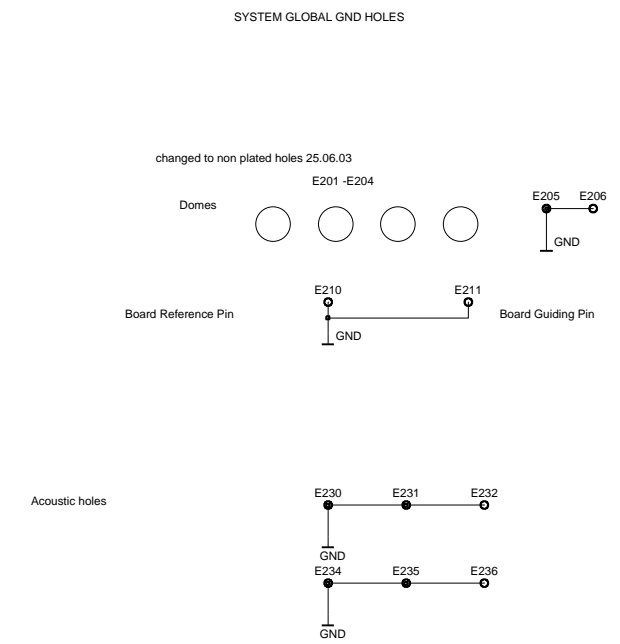
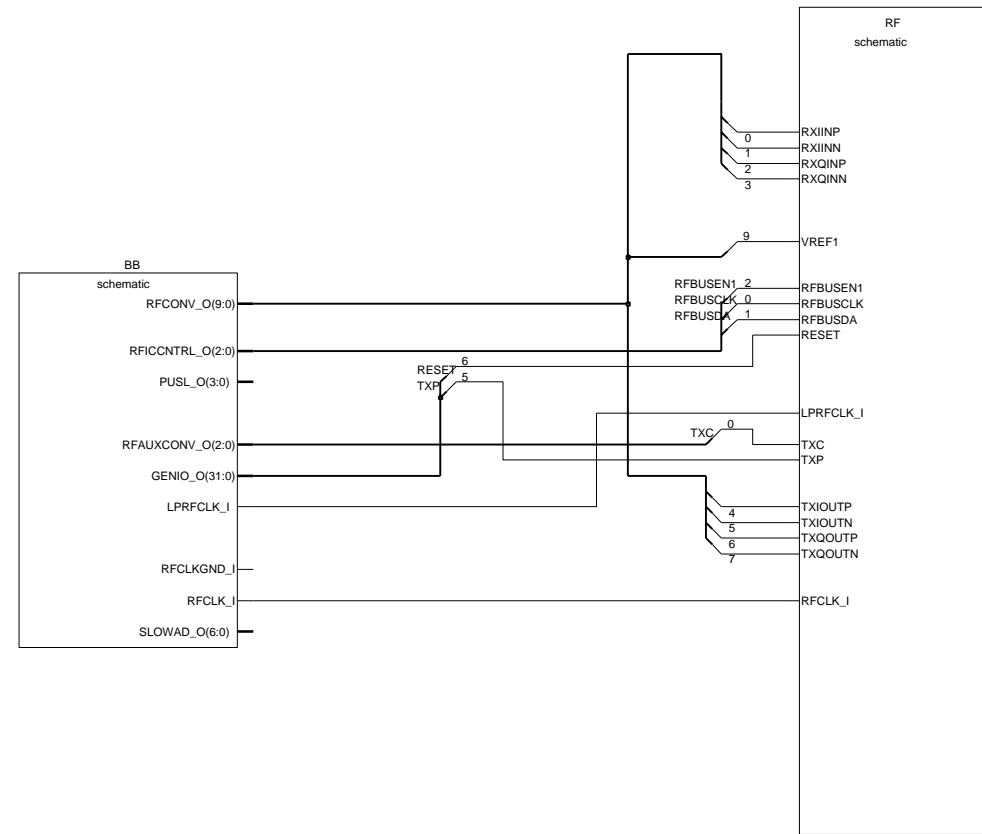
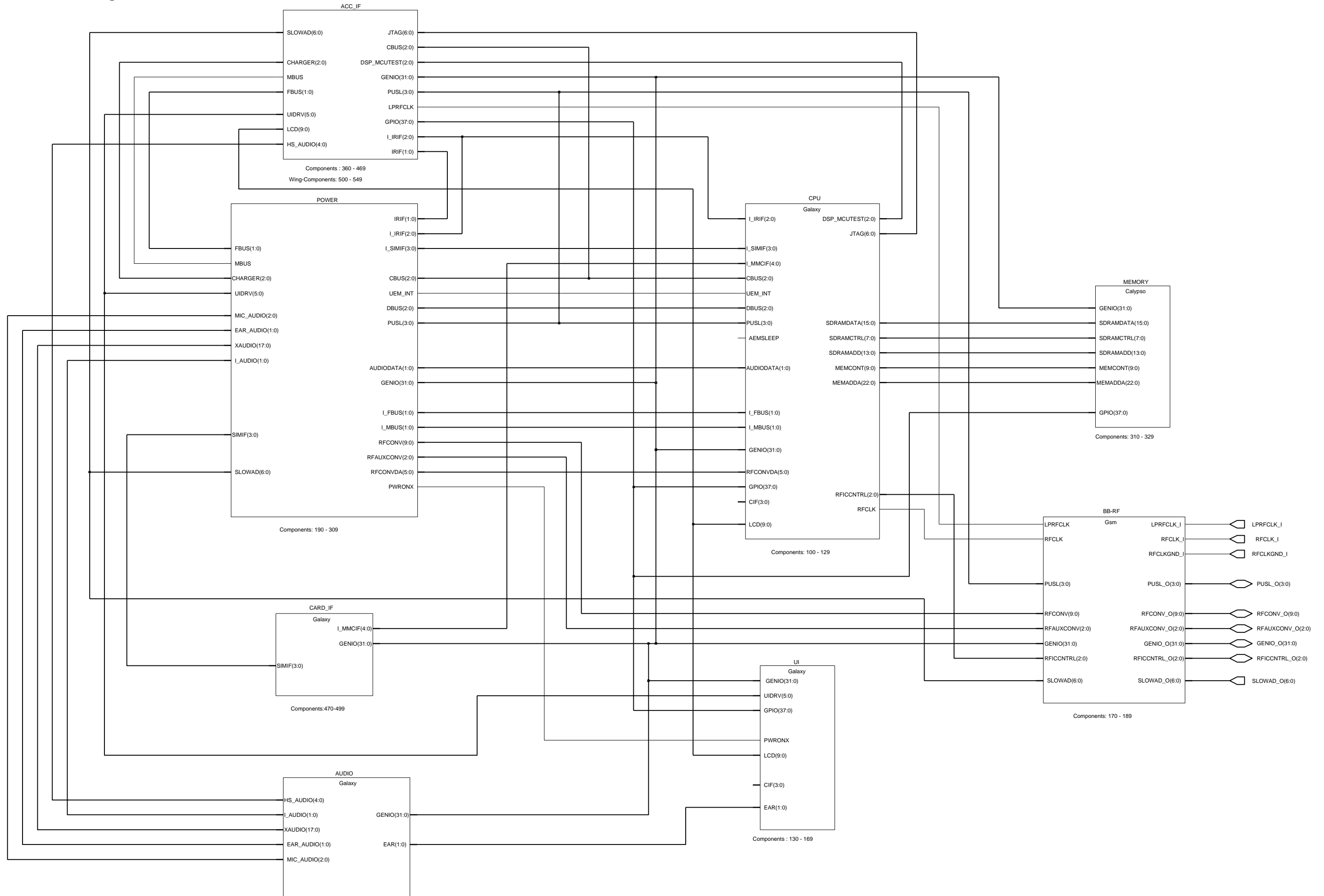


8 - SCHEMATICS

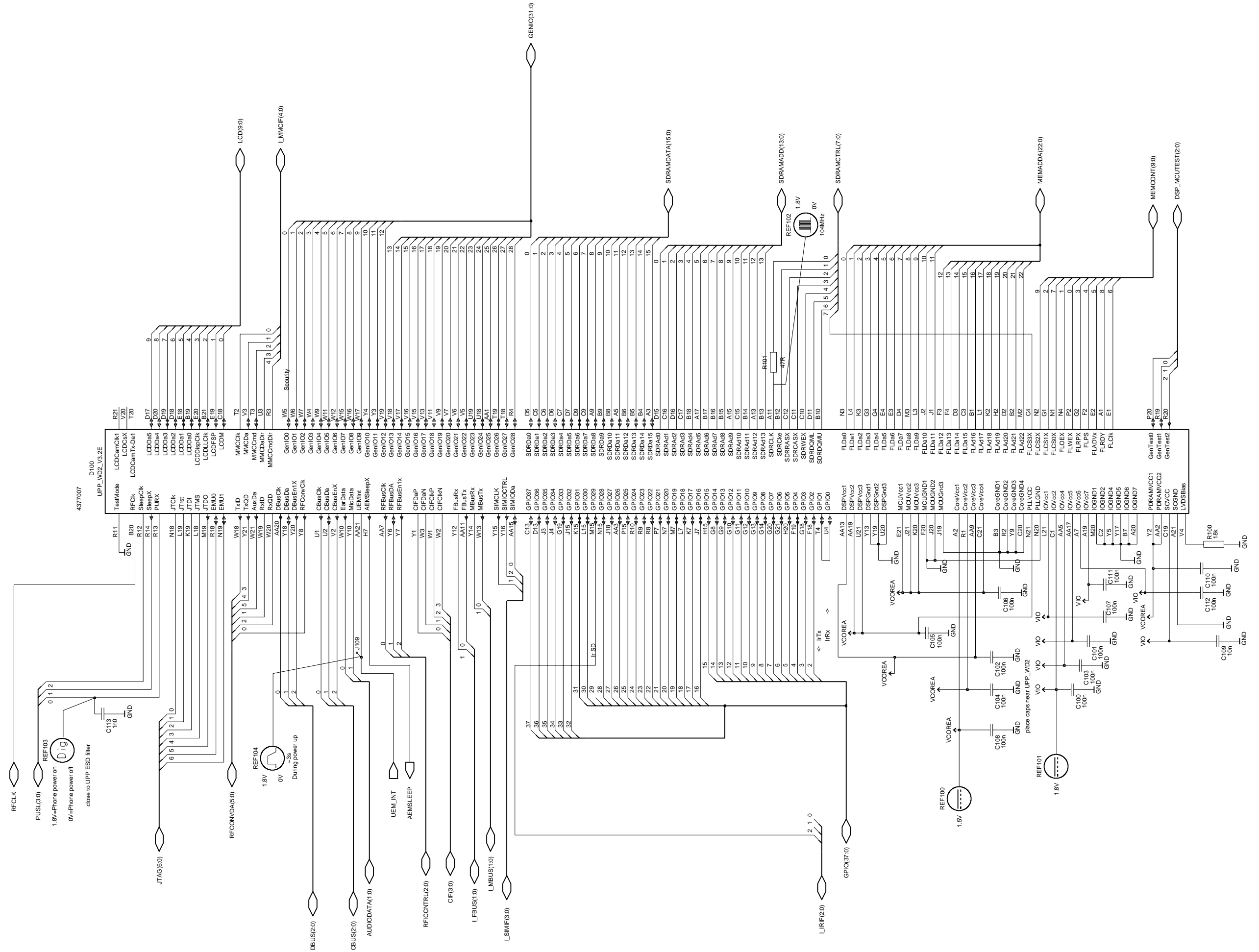
Table of Contents	Page no
BB/RF v.0.0 ed.19	2
Baseband block diagram v.1.0 ed.18	3
UPP WD2 schematic diagram v.1.0 ed. 12	4
Memories diagram v. 0.1 ed.11	5
Audio diagram, v. 1.0 ed.11	6
GSM BB-RF interface v.0.1 ed.8	7
MMC card interface diagram, v.0.0 ed.13	8
Accessory interface v.1.0 ed.13	9
Bluetooth BT202 diagram v.1.0 ed.11	10
IR module v.1.0 ed.8	11
System connector diagram, v.1.0 ed.16	12
Power diagram, v. 1.0 ed.12	13
Test interface diagram v. 0.1 ed.7	14
UEM diagram, v.1.0 ed.16	15
Aux_Power diagram, v.1.0 ed.11	16
RF/BB connection, v.0.0 ed.12	17
Power amplifier diagram, v.0.0 ed.9	18
RF Mjoelner ASIC diagram v.0.0 ed.11	19
Probe test points v.0.0 ed.2	20
Layout diagram 1CK_10a, top	21
Layout diagram 1CK_10a, bottom	22
User Interface diagram 1CJ, v.1.0 ed.19	23



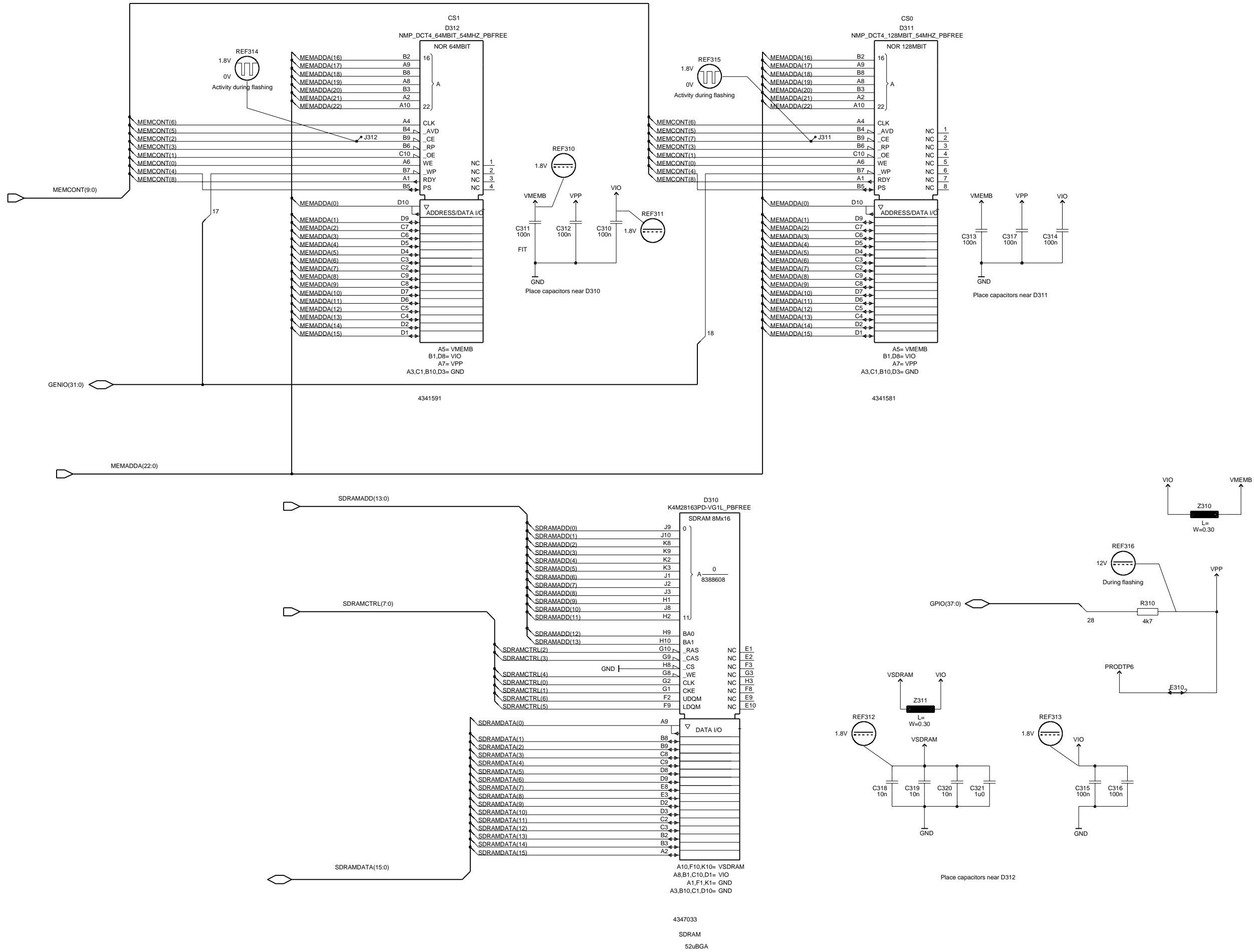
Baseband block diagram v.1.0 ed.18



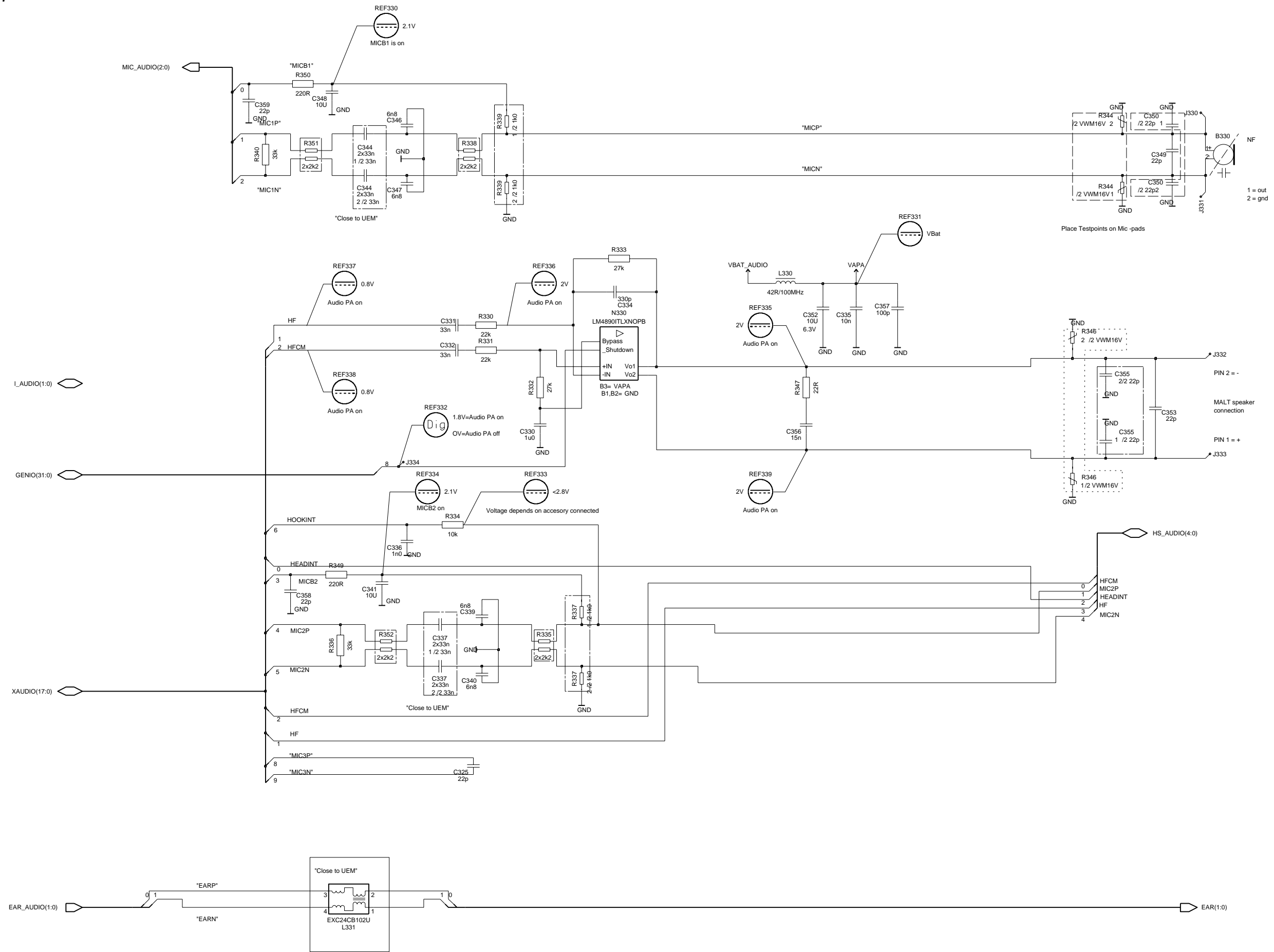
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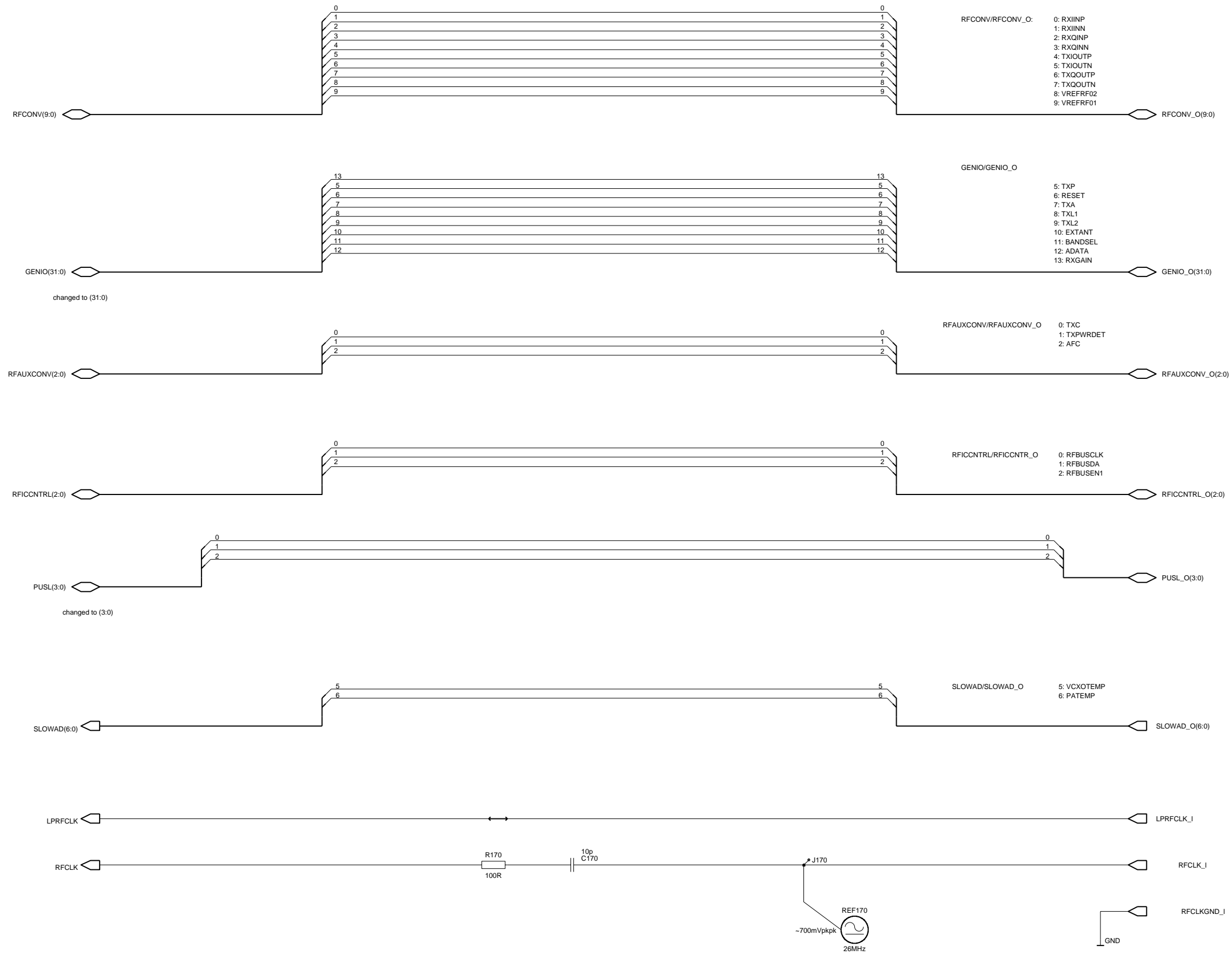


Memories diagram v. 0.1 ed.11

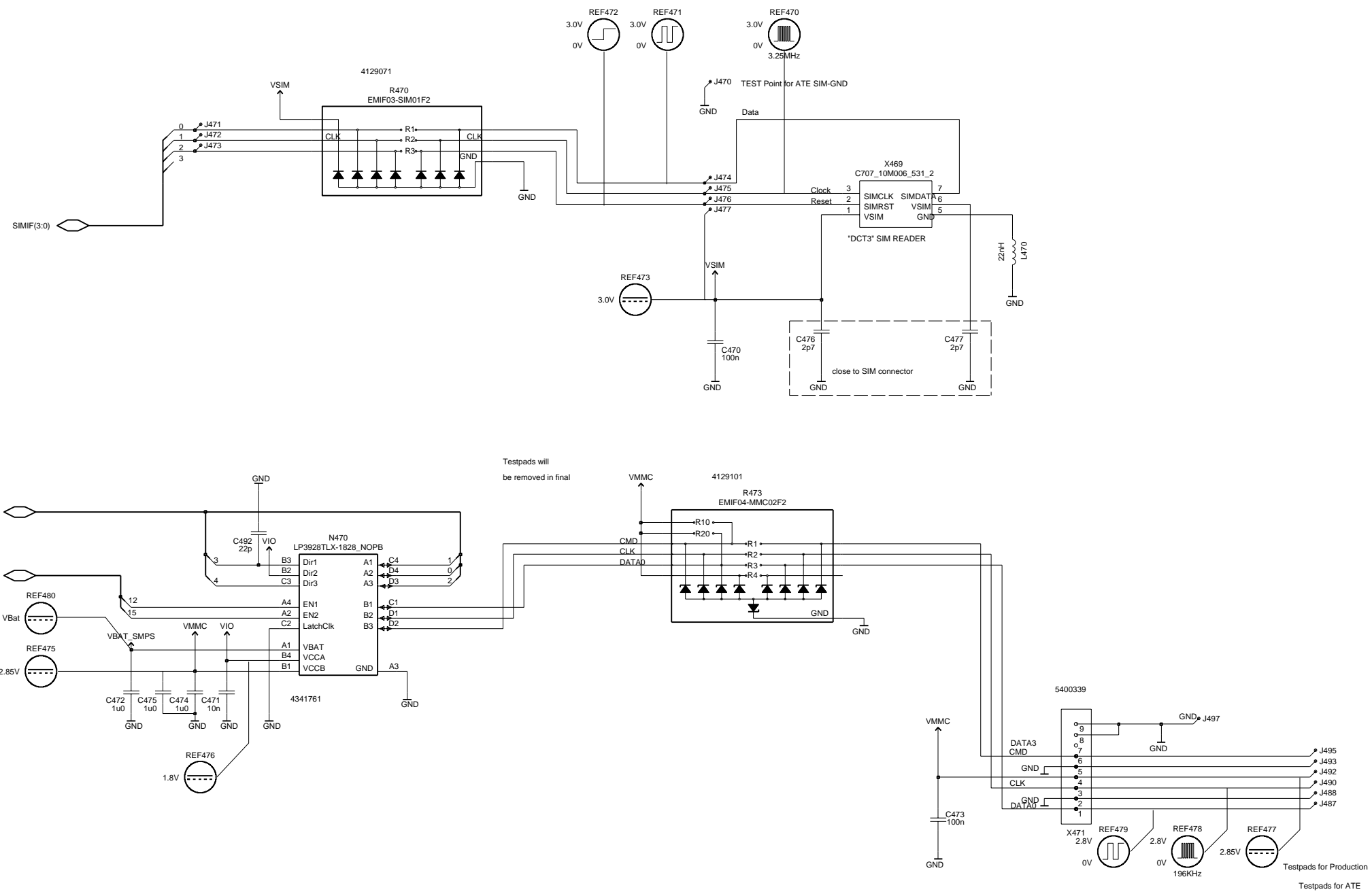


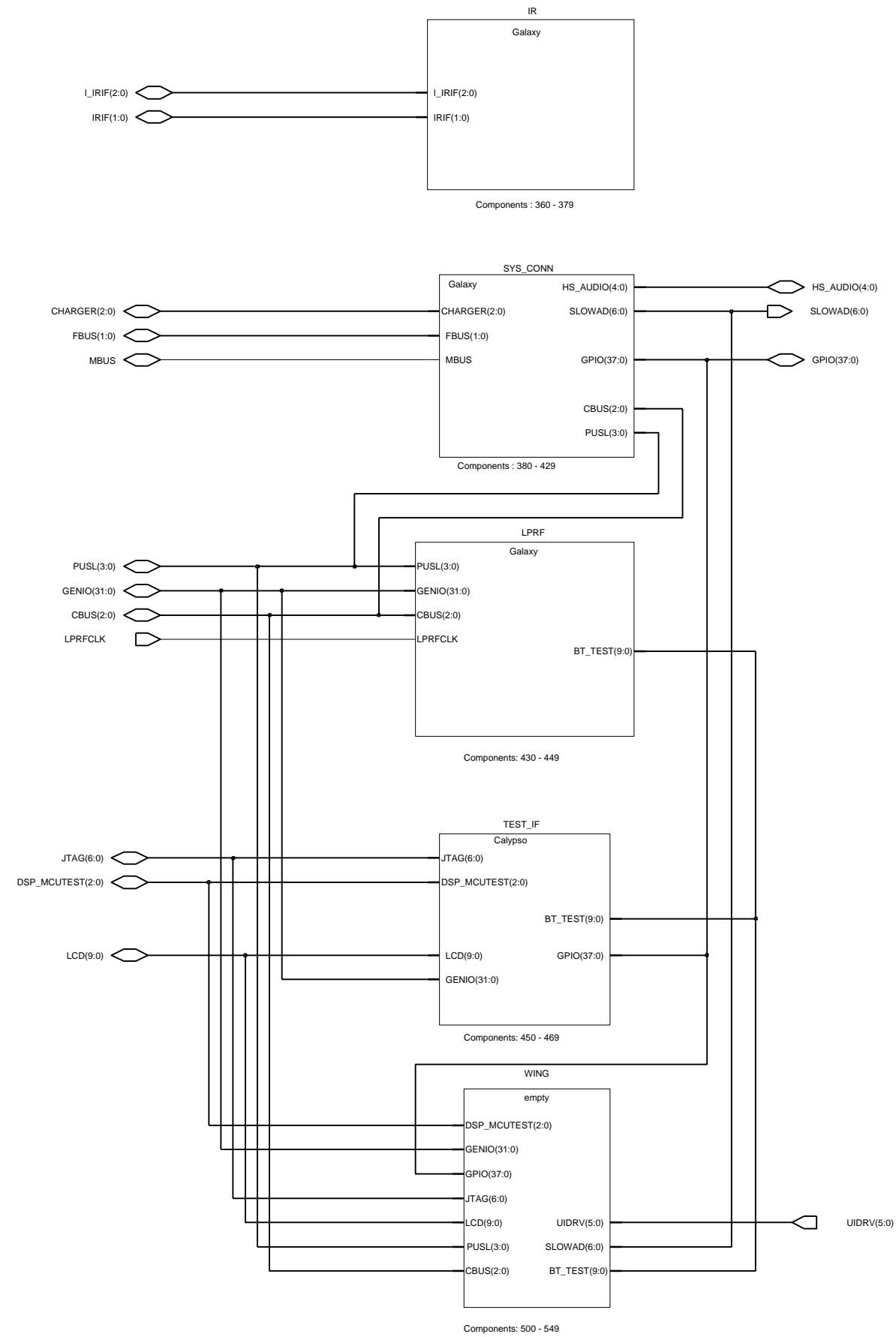
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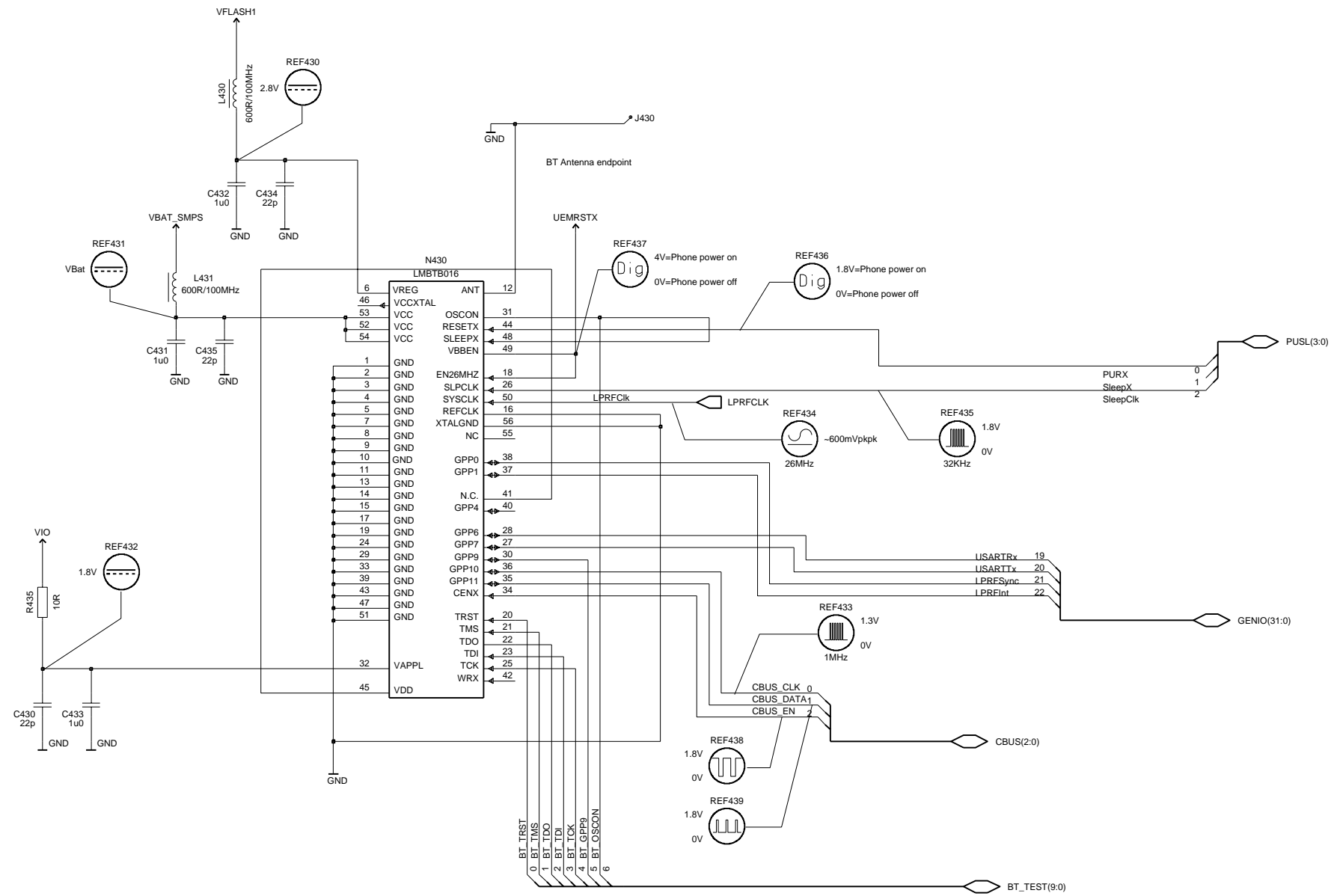


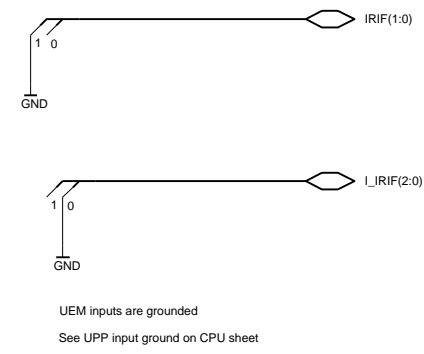


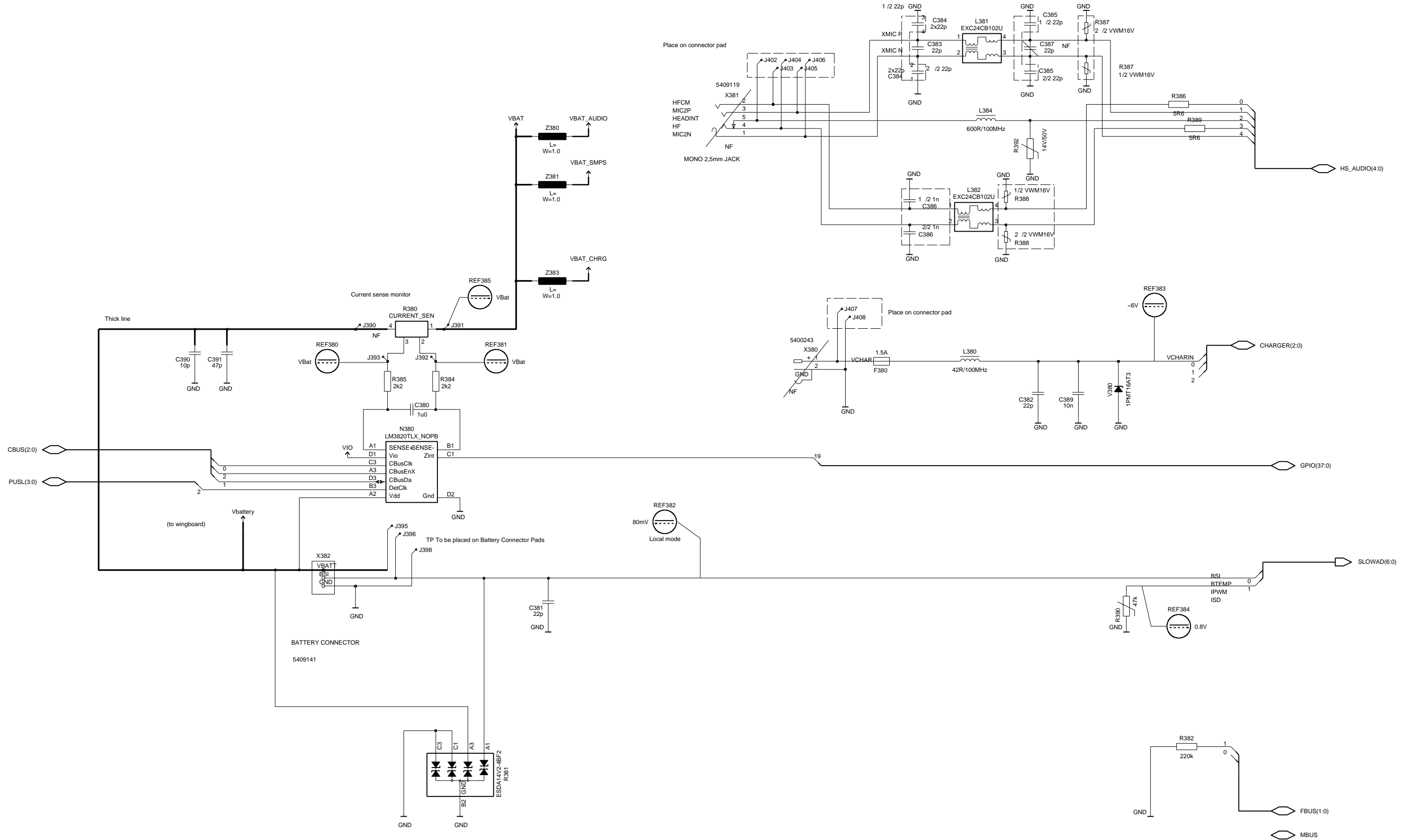
MMC card interface diagram, v.0.0 ed.13



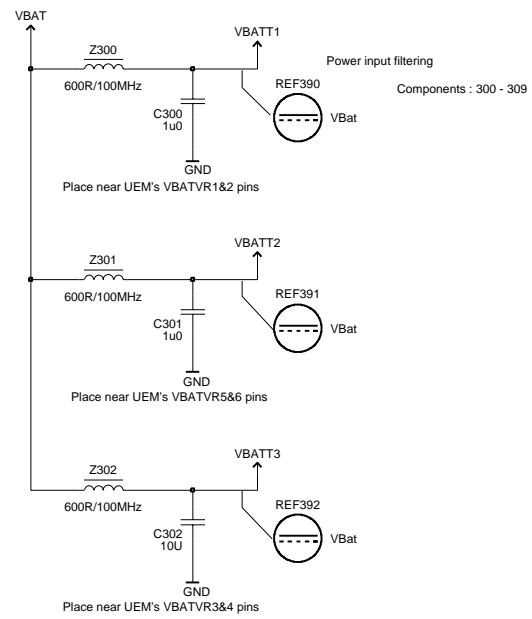
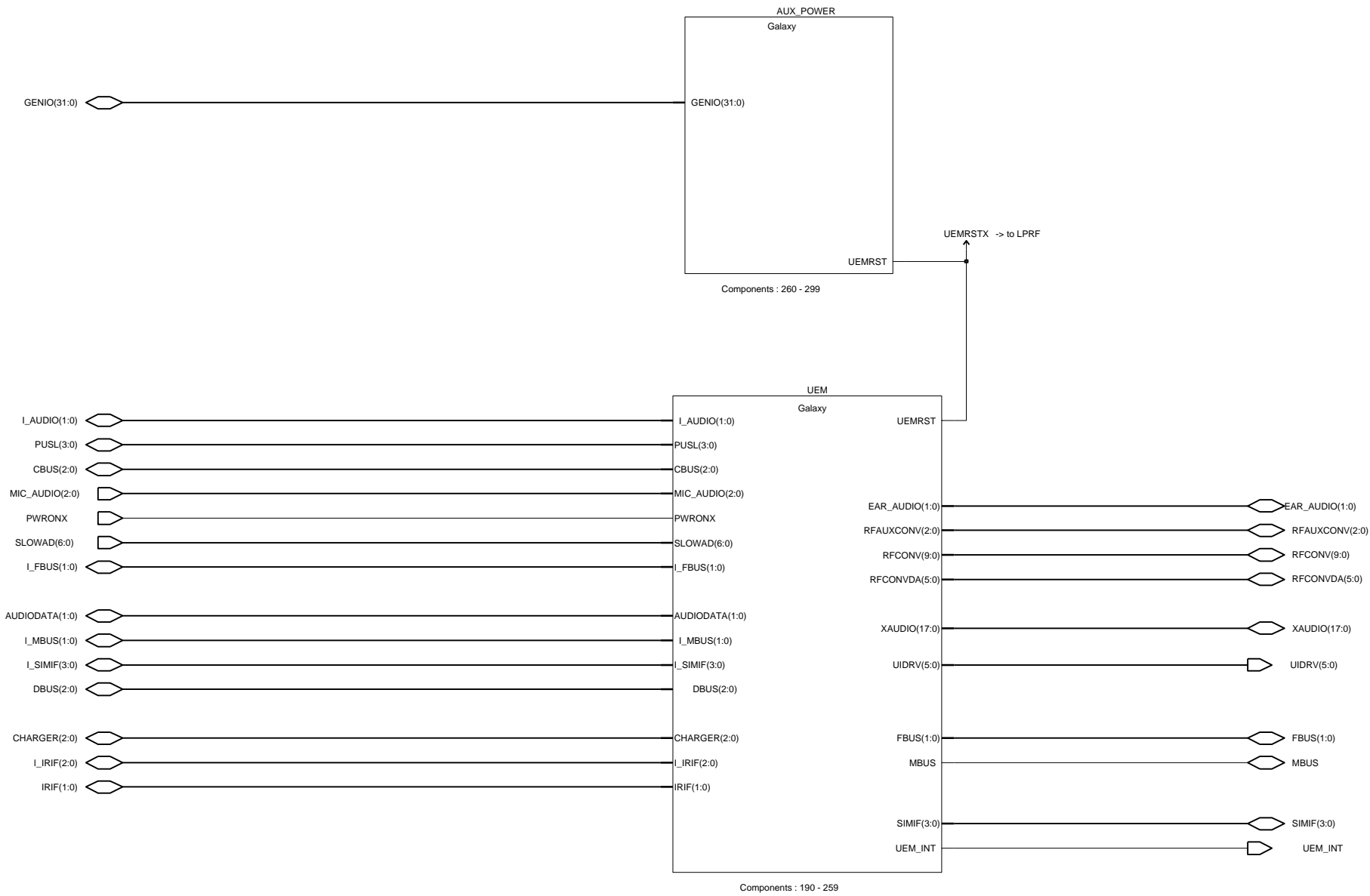




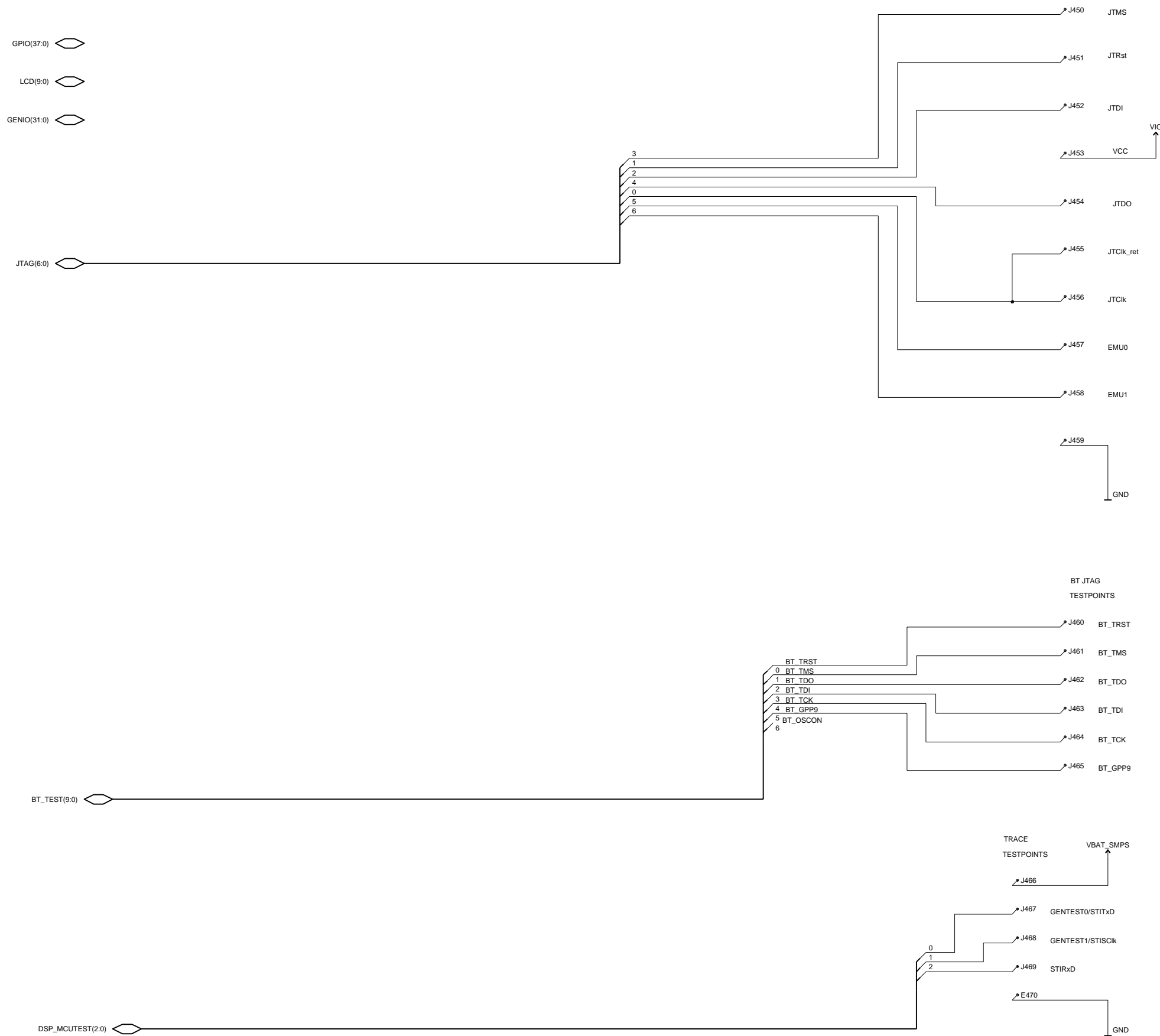




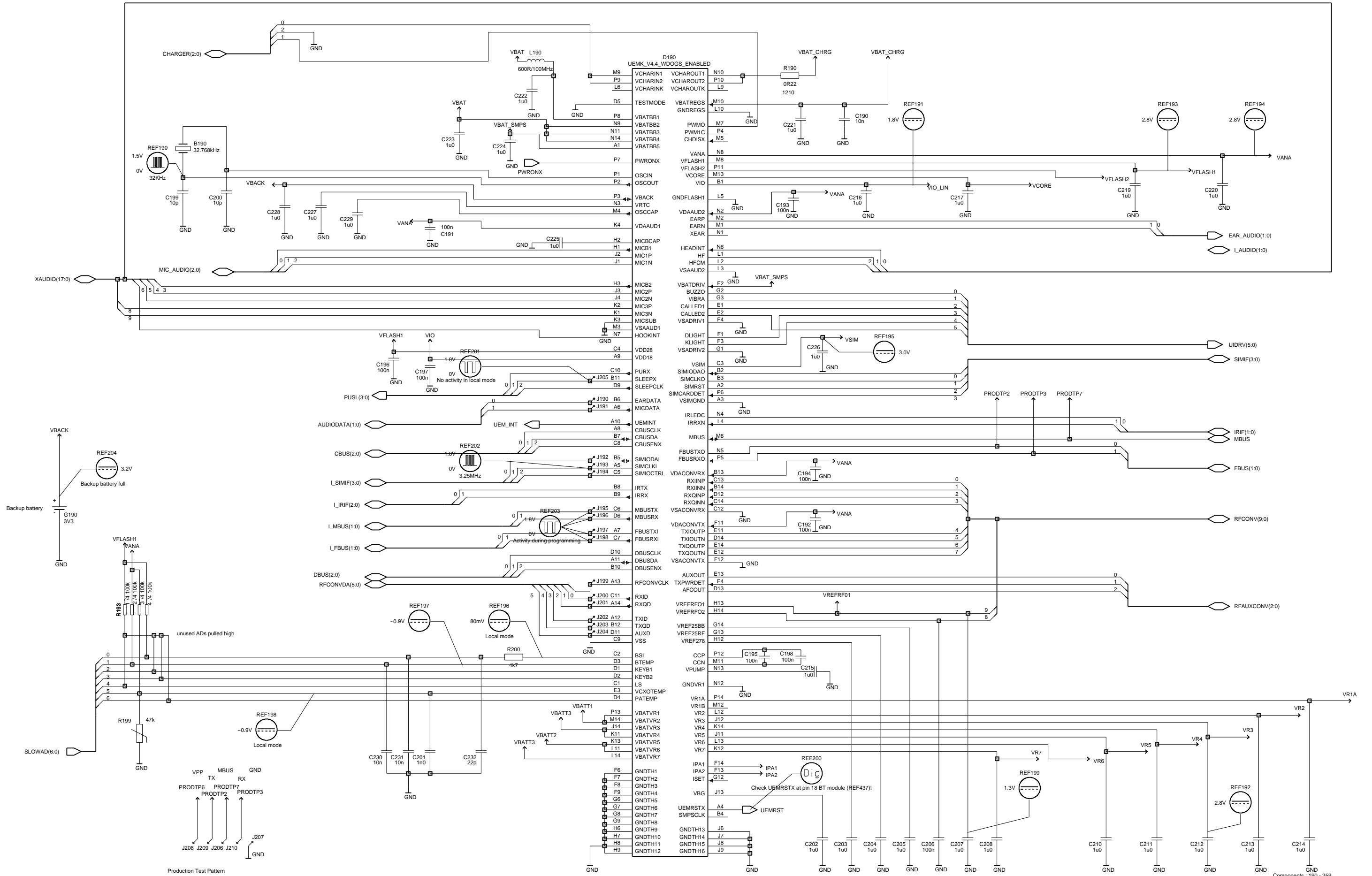
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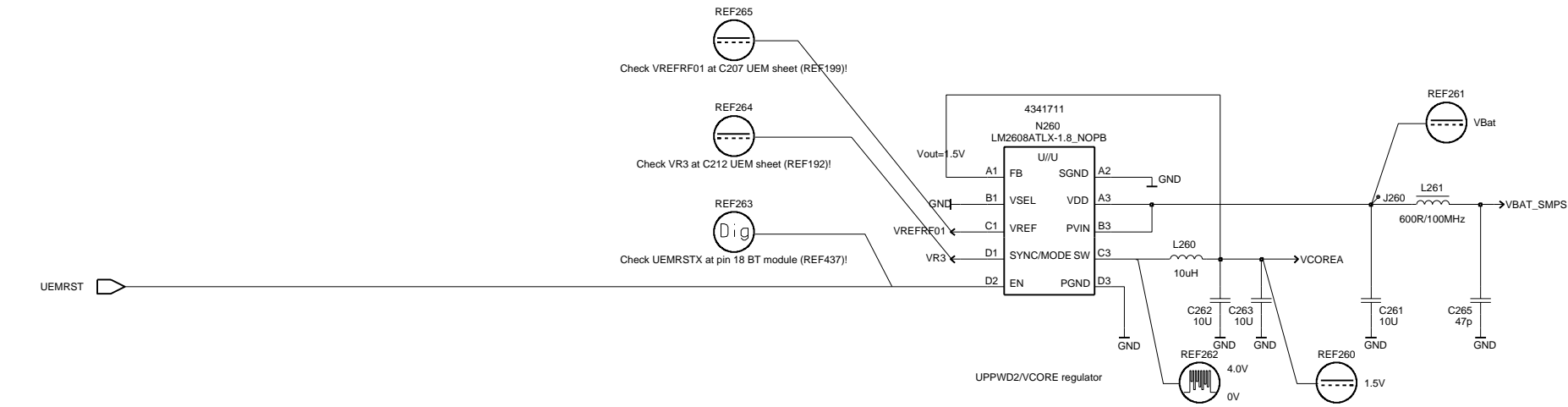
Test interface diagram v. 0.1 ed.7



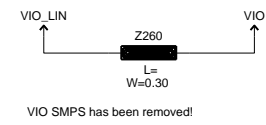
UEM diagram, v.1.0 ed.16

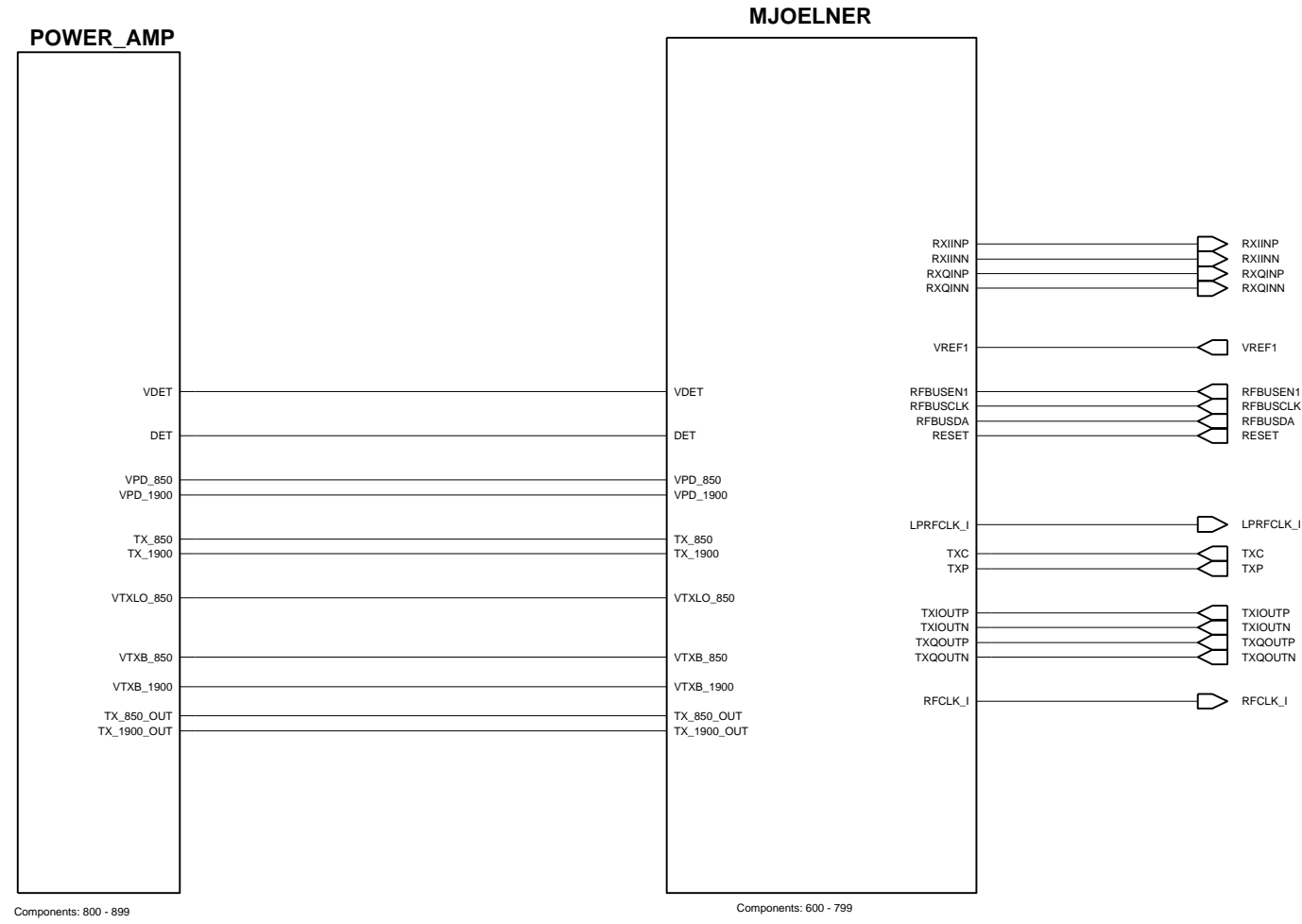


Aux_Power diagram, v.1.0 ed.11

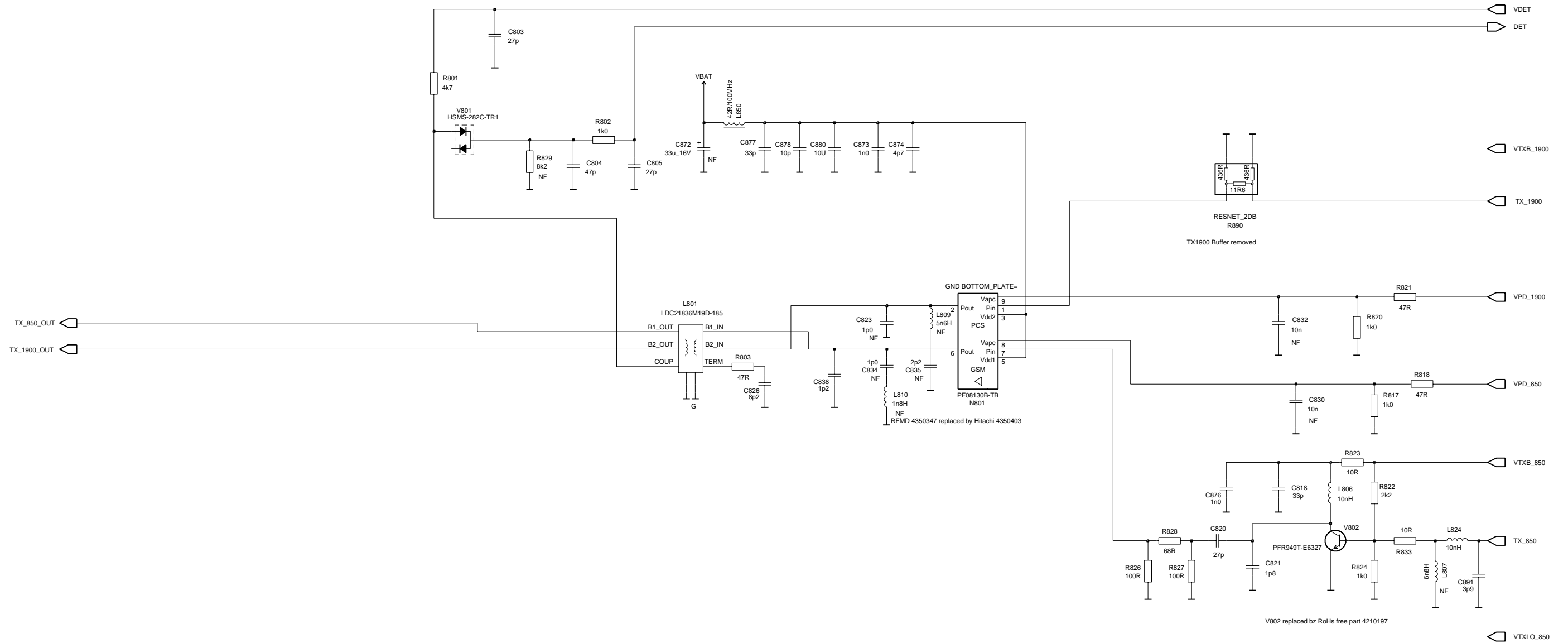


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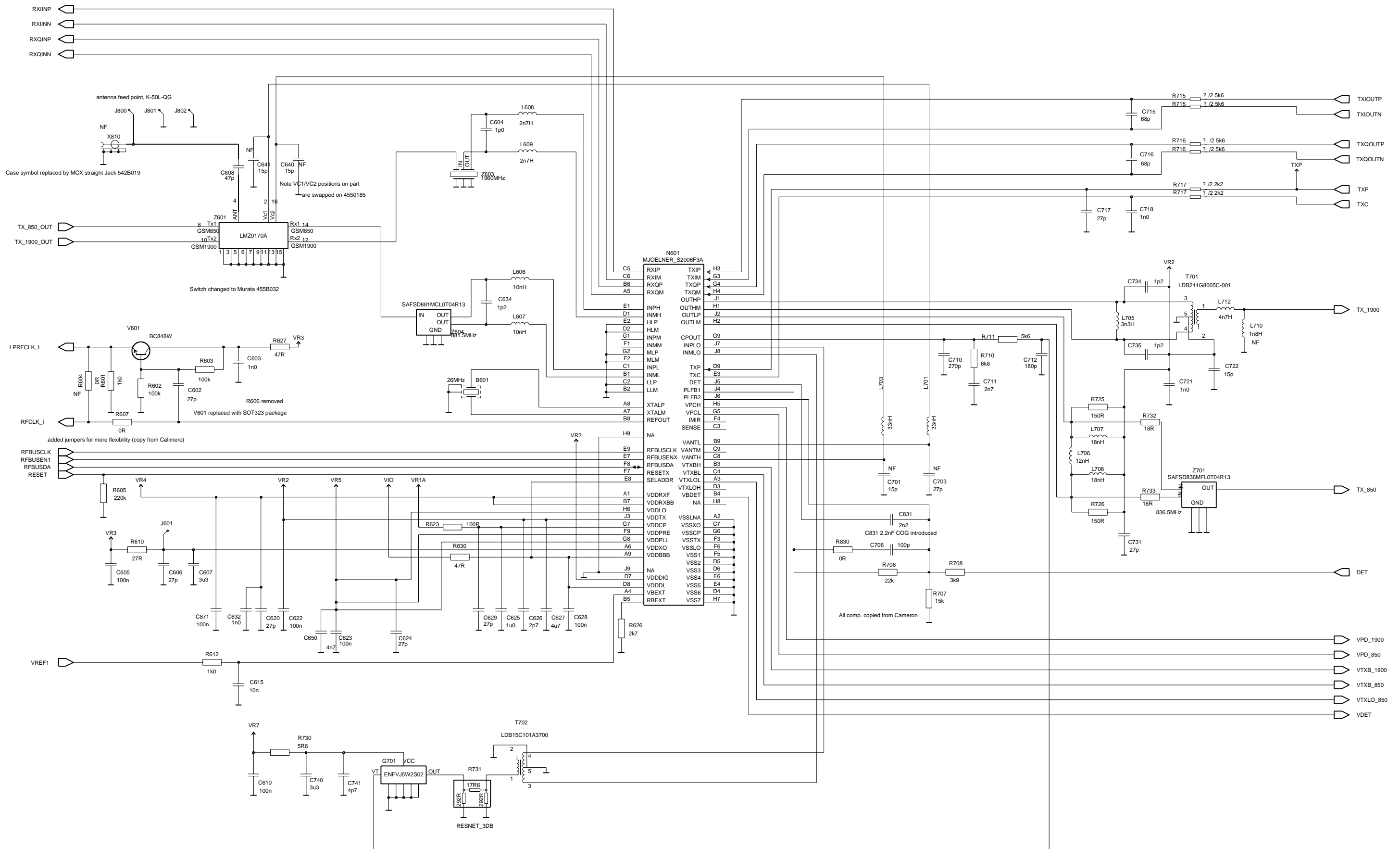


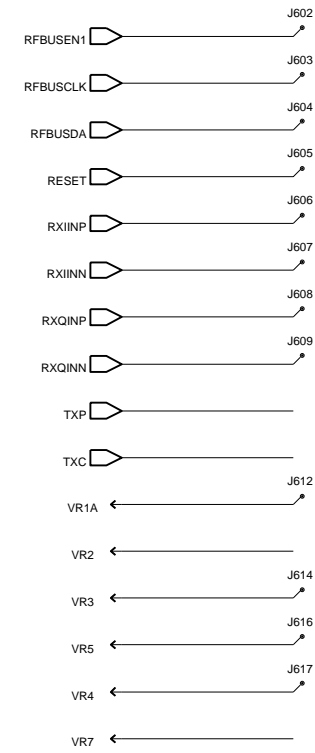
Power amplifier diagram, v.0.0 ed.9



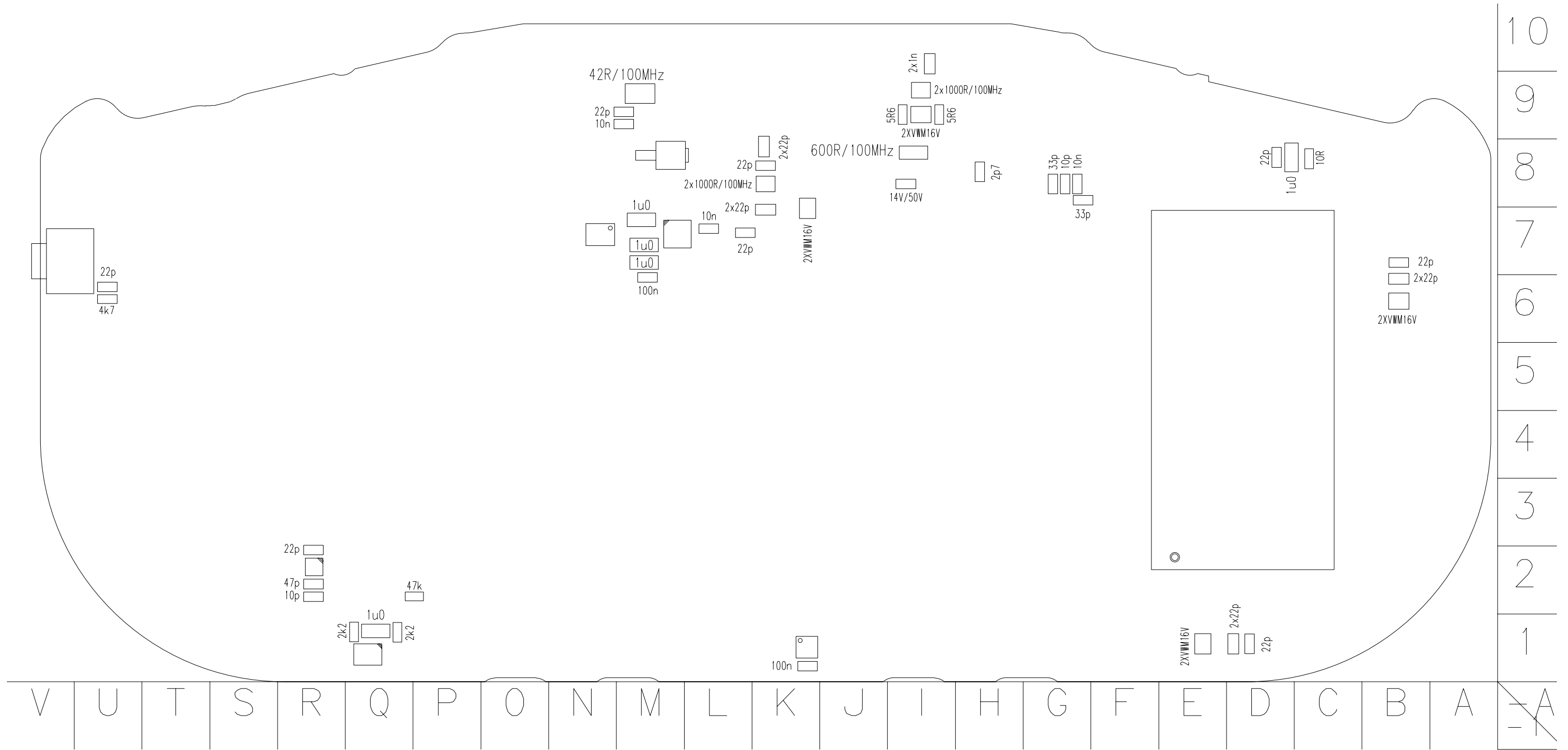
Components: 800 - 899

RF Mjoelner ASIC diagram v.0.0 ed.11

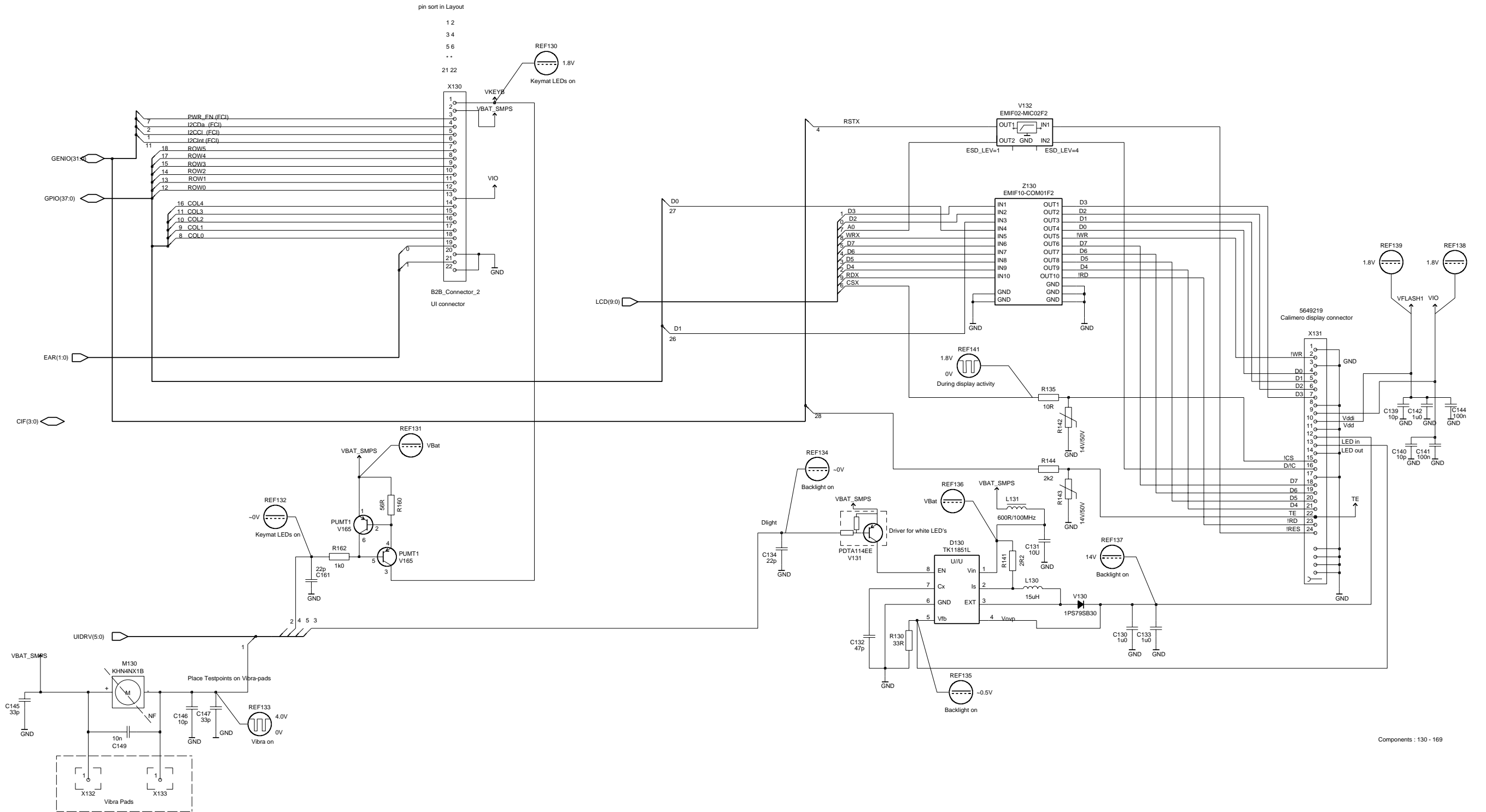
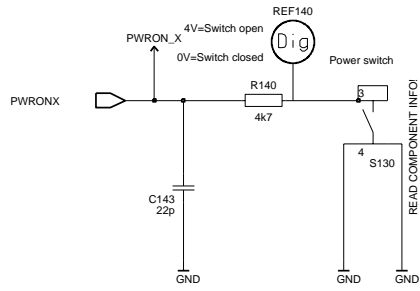




Layout diagram 1CK_10a, bottom



User Interface diagram 1CJ, v.1.0 ed.19



Components : 130 - 169